Amendments to the Specification:

Please replace the paragraph beginning on page 4, line 20, and continuing to page 5, line 5, with the following rewritten paragraph:

According to further aspect of the present invention, there is provided a real time recording/reproducing system for recording a digital data in a recorder obtained by converting an analog image signal, and reproducing the recorded the digital data through in the analog data ~~fromat~~ <u>format</u> comprising steps of: storing the digital data in a first frame memory; compressing the output of the first frame memory; decompressing the digital data read out from the recorder; storing the decompressed data in a second memory; controlling the frame rate of the compressed data to be constant by executing a frame interpolating processing; and executing a frame skipping processing when it becomes unable to execute full frame real time decompression processing.

Please replace the paragraph beginning on page 9, line 22, and continuing to page 10, line 9, with the following rewritten paragraph:

The functions of the individual elements in the real time recording/reproducing system shown in Fig. 1will now be described. The ADC 102 converts analog signal externally inputted from the input terminal 101 to digital form, captures the digital image data thus obtained in units of frames, and feeds out the captured data to the frame memory 103 for storage therein. The compression processing module 104 compresses the frame data stored in the frame memory 103, and feeds out the compressed data to the recorder 105 for recording therein. The decompression processing module 106 decompresses the compressed digital data recorded in the recorder 105 to data covering a given time, and feeds out the decompressed data to the frame memory 107 for storage therein. The DAC 108 converts the digital frame data stored in the frame memory 107 to analog form for outputting the analog data thus obtained from the output terminal ~~108~~ <u>109</u>.

Please replace the paragraph on page 10, lines 10 to 29, with the following rewritten paragraph:

When the system becomes unable to execute full frame real time processing due to the CPU performance insufficiency, the frame rate controller 110 executes a control process of controlling the frame rate of dataread out from the frame memory 103 to the compression processing module 104 to be constant. The compression processing ~~frame~~ module 104 has a frame thinning-out function of thinning out some frames in the compression processing to reduce the actual frame rate while holding a constant standard frame rate with omitting a part of the frame compression process. The decompression processing module 106 has a frame skipping function of skipping some ~~frame~~ frames in the decompression processing for reproduction synchronous to voice data with omitting a part of the frame decompression process. As for the frame thinning-out process in the compression processing module 104 and the frame skipping function in the decompression processing module 106, further functions are provided that these processes are executed preferentially from frame-interpolated frames.

Please replace the paragraph on page 11, lines 1 to 18, with the following rewritten paragraph:

The operation of the real time recording/reproducing system shown in Fig. 1 will now be described in greater ~~details~~ detail. Referring to Fig. 1, when the ADC 102 becomes unable to execute full frame real time capturing process due to the CPU performance insufficiency, the frame data stored in the frame memory 103 becomes discontinuous such that some frames are dropped out. Fig. 2 shows an example of such drop-out. In this example, frames (n + 1), (n + 4) and (n + 5) are dropped out. The frame rate controller 110 executes frame drop-out judgment by obtaining time data of each captured frame from the ADC 102. The controller 110 then controls the frame rate of the input to the compression processing module 104 to be constant by executing a frame interpolation process concerning the dropped-out frames. The controller 110 normally designates a pointer of a

memory area with each frame data stored therein to the compression processing module 104.

Please replace the paragraph beginning on page 12, line 8, and continuing to page 13, line 3, with the following rewritten paragraph:

In the real time recording/reproducing system shown in Fig. 1, the compression processing module 104 executes digital compression processing in a compressing system, which conforms to, for instance, MPEG (~~motion picture~~ Motion Picture Experts Group compressing system) standards. When the module 104 becomes unable to execute full frame real time compression processing, it executes frame thinning-out processing for reducing the actual frame rate by thinning out some frames in the compression processing while holding a fixed regular frame rate as prescribed in the MPEG standards. The CPU load in the compression processing is thus reduced so as to be able to continue the real time processing. In the MPEG standards, three different "frame types", i.e., types of frames subjected to the compression processing, are defined, that is, with omitting a part of the frame compression process "I frame" which does not require any reference frame and may serve as a reference frame for other frames, "P frame" which requires a reference frame and may also serve as a reference frame for other frames, and "B frame" which requires a reference frame and does not serve as any reference frame. The frame thinning-out processing is executed with respect to "B frames", which are always non-reference frames among the above three different frame types.